

IN THE CLAIMS:

The text of all pending claims, (including withdrawn claims) is set forth below. Cancelled and not entered claims are indicated with claim number and status only. The claims as listed below show added text with underlining and deleted text with ~~strike through~~. The status of each claim is indicated with one of (original), (currently amended), (cancelled), (withdrawn), (new), (previously presented), or (not entered).

Please CANCEL claim 15 without prejudice or disclaimer; and AMEND claims 1, 5, 12-14, 16-17, 20, 22, 23 and 25, in accordance with the following:

Claim 1 (Currently Amended): An electroluminescent display EL device comprising:

- a substrate;
- a first electrode unit comprising:
 - first electrodes formed on the substrate as a plurality of parallel evenly spaced lines, and
 - first electrode terminals connected to the respective first electrodes;
- a second electrode unit comprising:
 - second electrodes formed in an orthogonal direction with respect to the first electrodes over the first electrodes, and
 - second electrode terminals connected to the respective second electrodes;
- an emission area formed where the first electrodes intersect the second electrodes;
- an electroluminescent layer disposed between the first electrodes and the second electrodes in the emission area;
- an inter insulating layer provided under the electroluminescent layer and covering a space between each of the plurality of lines of the first electrodes and an edge portion of a top surface of each of the plurality of lines of the first electrodes; ~~and~~
- an outer insulating layer between the emission area and the second electrode terminals,; wherein the outer insulating layer comprises an insulating material formed to contact at least an edge of the second electrode terminals facing the emission area so as to reduce a steepness of a step between the second electrode terminals and the substrate; and
- a buffer layer provided over a top surface of the substrate to maintain smoothness of the

top surface of the substrate and to prevent impurities from being introduced from the substrate.

Claims 2-3 (Cancelled):

Claim 4 (Original): The EL device of claim 1, wherein the substrate comprises glass or plastic.

Claim 5 (Currently Amended): The EL device of claim 1, wherein ~~each of the~~ second electrode terminals ~~comprises a first~~ comprise a lower terminal portion made of indium tin oxide (ITO) formed on the substrate, and ~~a second an upper~~ terminal portion made of chrome (Cr) formed on the lower terminal portion with a step facing the emission area.

Claim 6 (Original): The EL device of claim 1, wherein the first electrode terminals are integrally formed with the first electrodes.

Claim 7 (Original): The EL device of claim 1, wherein the outer insulating layer covers the edge of each of the second electrode terminals facing the emission area.

Claim 8 (Original): The EL device of claim 1, wherein the outer insulating layer covers at least an edge of the first electrode closest to the second electrode terminals covered by the outer insulating layer.

Claim 9 (Original): The EL device of claim 7, further comprising via holes formed at portions of the outer insulating layer covering the edge of the second electrode terminals, so that the second electrodes and the second electrode terminals are electrically connected to each other, respectively, through the via holes.

Claim 10 (Original): The EL device of claim 7, wherein the second electrodes pass over the outer insulating layer to contact the second electrode terminals.

Claim 11 (Original): The EL device of claim 1, wherein the second

electrodes cover the outer insulating layer.

Claim 12 (Currently Amended): The EL device of claim 1, further comprising a ~~first~~another buffer layer insulated from the first electrodes and the second electrode terminals, ~~and wherein the first buffer layer is formed~~disposed between the outer insulating layer and the substrate.

Claim 13 (Currently Amended): The EL device of claim 12, wherein the ~~first~~another buffer layer comprises a same material as the first electrodes.

Claim 14 (Currently Amended): The EL device of claim 13, wherein the ~~first~~another buffer layer and the first electrodes are comprised of ITO.

Claim 15 (Cancelled):

Claim 16 (Currently Amended): The EL device of claim ~~15~~1, wherein the ~~second~~ buffer layer comprises SiO₂.

Claim 17 (Currently Amended): An electroluminescent display EL device comprising:

- a substrate;
- a first electrode unit comprising:
 - first electrodes formed on the substrate as a plurality of parallel evenly spaced lines, and
 - first electrode terminals connected to the respective first electrodes;
- a second electrode unit comprising:
 - second electrodes formed in an orthogonal direction with respect to the first electrodes over the first electrodes, and
 - second electrode terminals connected to the respective second electrodes;
- an emission area formed where the first electrodes intersect the second electrodes;
- an electroluminescent layer disposed between the first electrodes and the second

electrodes in the emission area; ~~and~~

an insulating layer formed under the electroluminescent layer; and

a buffer layer provided on a top surface of the substrate to maintain smoothness of the top surface of the substrate and to prevent impurities from being introduced from the substrate;

wherein the insulating layer is provided between each of a plurality of lines of the first electrodes and an edge portion of a top surface of each of the plurality of lines of the first electrodes, and at a space between the second electrode terminals and the first electrode adjacent thereto, and

wherein the insulating layer covers an edge of the second electrode terminals facing the emission area outside the emission area, ~~and reduces so as to reduce~~ a steepness of a step between the second electrode terminals and the substrate.

Claims 18-19 (Cancelled):

Claim 20 (Currently Amended): The EL device of claim 17, wherein the insulating layer covers the second electrode terminals and an edge of the first electrode adjacent to the second electrode terminals, and wherein each of the second electrode terminals comprises a lower terminal portion made of indium tin oxide (ITO), and an upper terminal portion made of chrome (Cr).

Claim 21 (Original): The EL device of claim 17, further comprising a buffer layer insulated from the first electrodes and the second electrode terminals, wherein the buffer layer is formed between a portion of the insulating layer and the substrate, said portion of the insulating layer covering a space between the second electrode terminals and the first electrode adjacent thereto.

Claim 22 (Currently Amended): A method of manufacturing an electroluminescent display EL device, the method comprising:

forming first electrode terminals and second electrode terminals along edges of a substrate;

forming first electrodes having a predetermined pattern, the first electrodes connected to the first electrode terminals;

forming an insulating layer covering at least a space between each of a plurality of lines of the first electrodes, an edge portion of a top surface of each of the plurality of lines of the first electrodes, and at a space between the second electrode terminals and the first electrode adjacent thereto;

forming an electroluminescent layer on at least each of the first electrodes; ~~and~~

forming second electrodes on the electroluminescent layer orthogonally with respect to the first electrodes,

wherein the second electrodes are connected to the second electrode terminals, ~~and~~

wherein the insulating layer covers an edge of the second electrode terminals facing an emission area ~~and reduces~~ as to reduce a steepness of a step between the second electrode terminals and the substrate, ~~and~~

wherein a buffer layer is formed on a top surface of the substrate to maintain smoothness of the top surface of the substrate and to prevent impurities from being introduced from the substrate.

Claim 23 (Currently Amended): The method of claim 22, wherein the insulating layer covers at least ~~a portion~~ an edge of each of ~~portion~~ the second electrode terminals, ~~and~~ wherein the second electrode terminals comprise a lower terminal portion formed on the substrate and an upper terminal portion formed on the lower terminal portion with a step facing the emission area.

Claim 24 (Previously Presented): The method of claim 23, further comprising forming via holes at portions of the insulating layer covering the second electrode terminals, during the forming of the insulating layer.

Claim 25 (Currently Amended): The method of claim 22, further comprising forming ~~a first~~ another buffer layer during the forming of the first electrodes, at a space between the second electrode terminals and the first electrode adjacent thereto using the same material as that of the first electrodes, so as to be spaced a predetermined distance apart from, and insulated from, the first electrodes and the second electrode terminals.